



Attorney Docket No. 1614.1383

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Yoshiki OKUMURA

Application No.: 10/773,232

ATTENTION

Confirmation No.: 3164

CERTIFICATE OF CORRECTION

Filed: February 9, 2004

BRANCH

U.S. Patent No.: 7,159,058

Issued: January 2, 2007

For: STATE INDICATING INFORMATION SETTING CIRCUIT AND STATUS BIT SETTING CIRCUIT

**REQUEST FOR CERTIFICATE OF CORRECTION**

✓ Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s) shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date: 2/27/07

By: J. Randall Becker  
J. Randall Becker  
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Certificate  
FEB 26 2007  
of Correction

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*FEB 26 2007*

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO : 7,159,058

DATED : January 2, 2007

INVENTOR(S) : Yoshiki OKUMURA

First Page, Column 2 (Other Publications), Lines 1-2, change "wikipedia.org.wiki/Flip-flop" to --wikipedia.org/wiki/Flip-flop--.

Column 19, Line 10, change "part:" to --part;--.

Column 19, Line 12, change "circuit:" to --circuit;--.

Column 20, Line 3, after "detecting" insert --a--.

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PATENT NO. 7,159,058

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